

FIG. 1

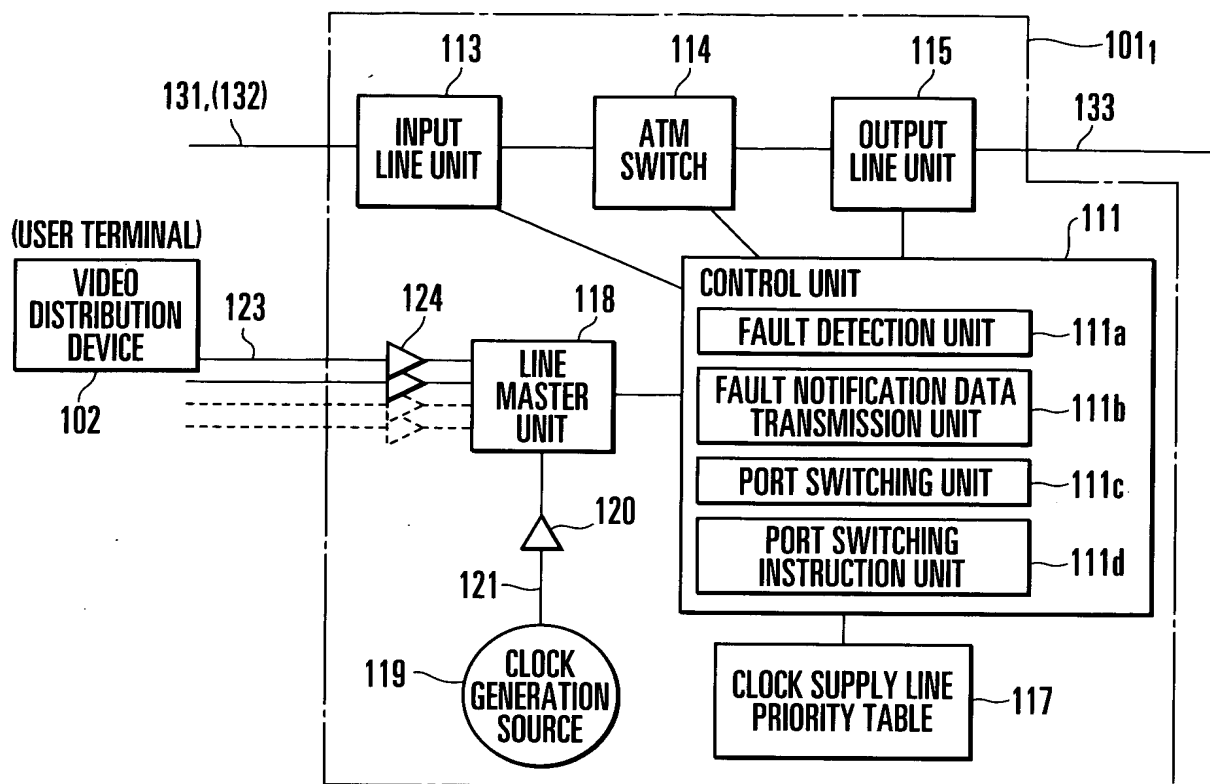
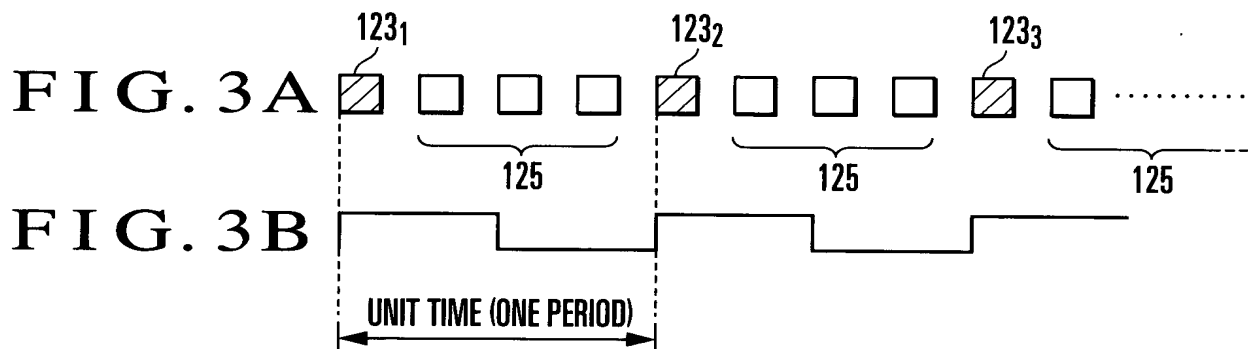


FIG. 2



PRIORITY	SECOND SWITCHING UNIT	THIRD SWITCHING UNIT	FOURTH SWITCHING UNIT
"1"	b	d	f
"2"	c	e	g

(CLOCK SUPPLY LINE PRIORITY TABLE)

FIG. 4

(CLOCK SUPPLY LINE FAULT DETECTION PROCESSING)

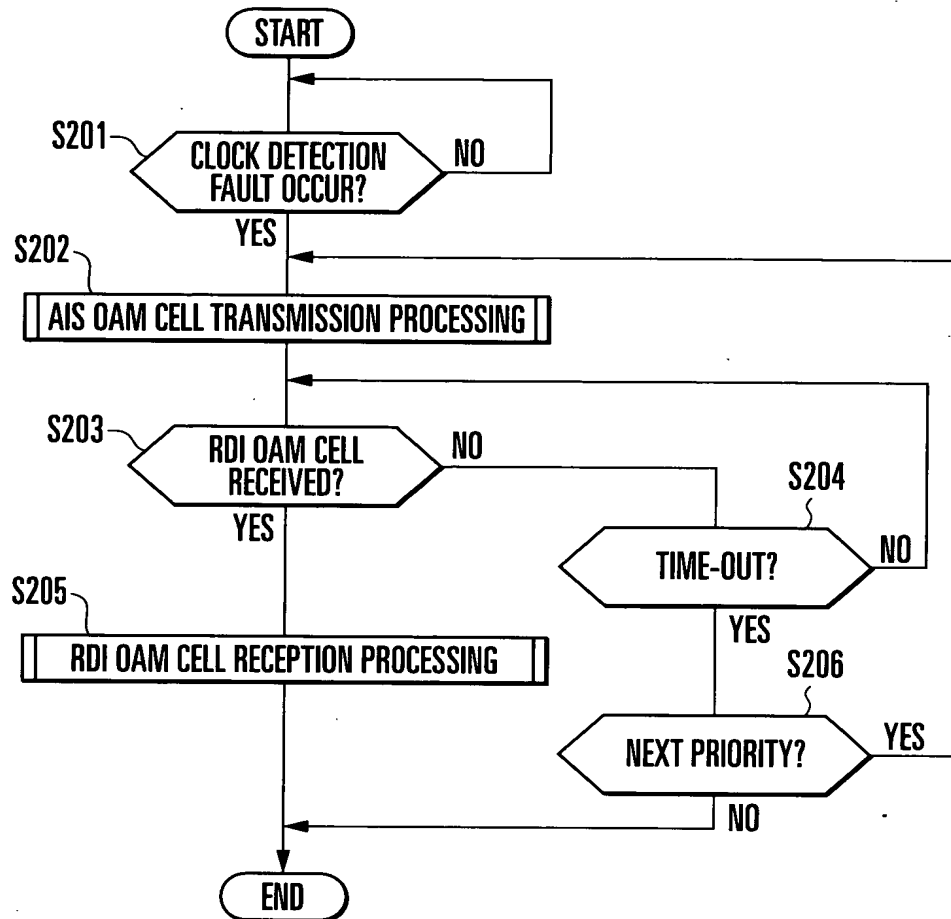


FIG. 5

(AIS OAM CELL TRANSMISSION PROCESSING)

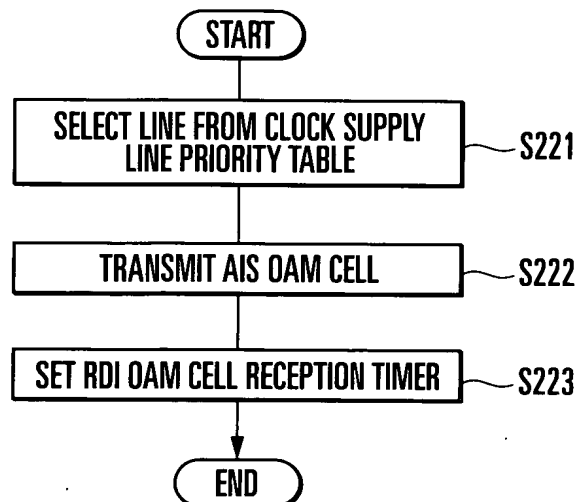


FIG. 6

(CLOCK SUPPLY LINE SWITCHING PROCESSING)

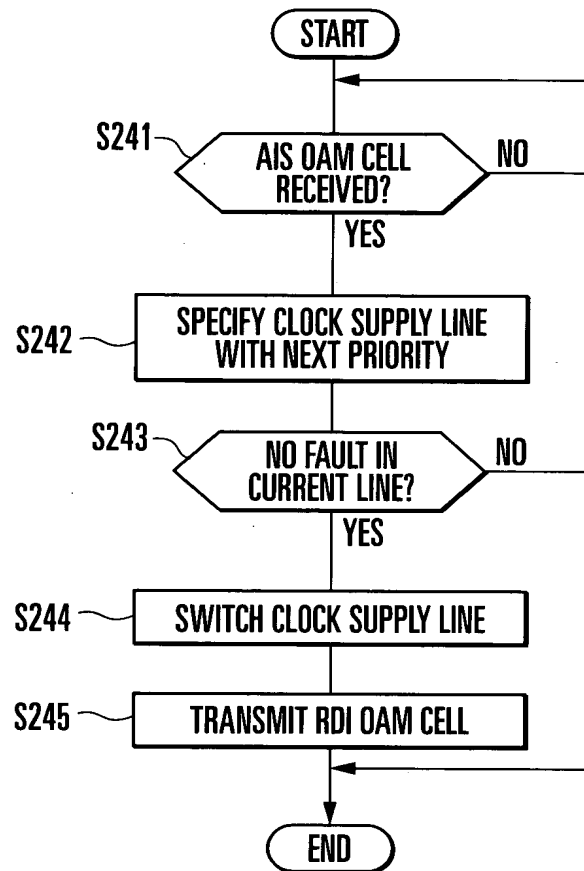


FIG. 7

(RDI OAM RECEPTION PROCESSING)

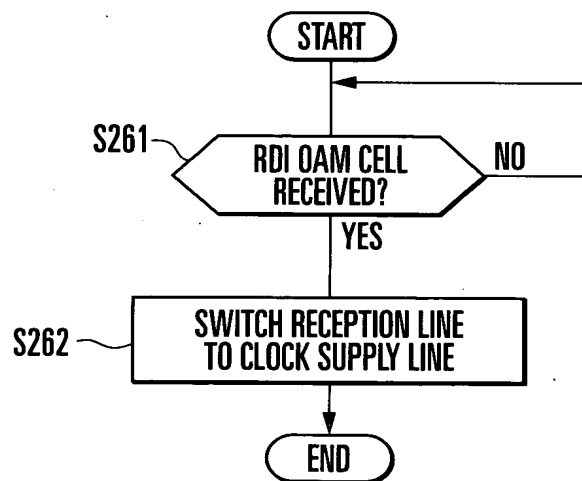


FIG. 8

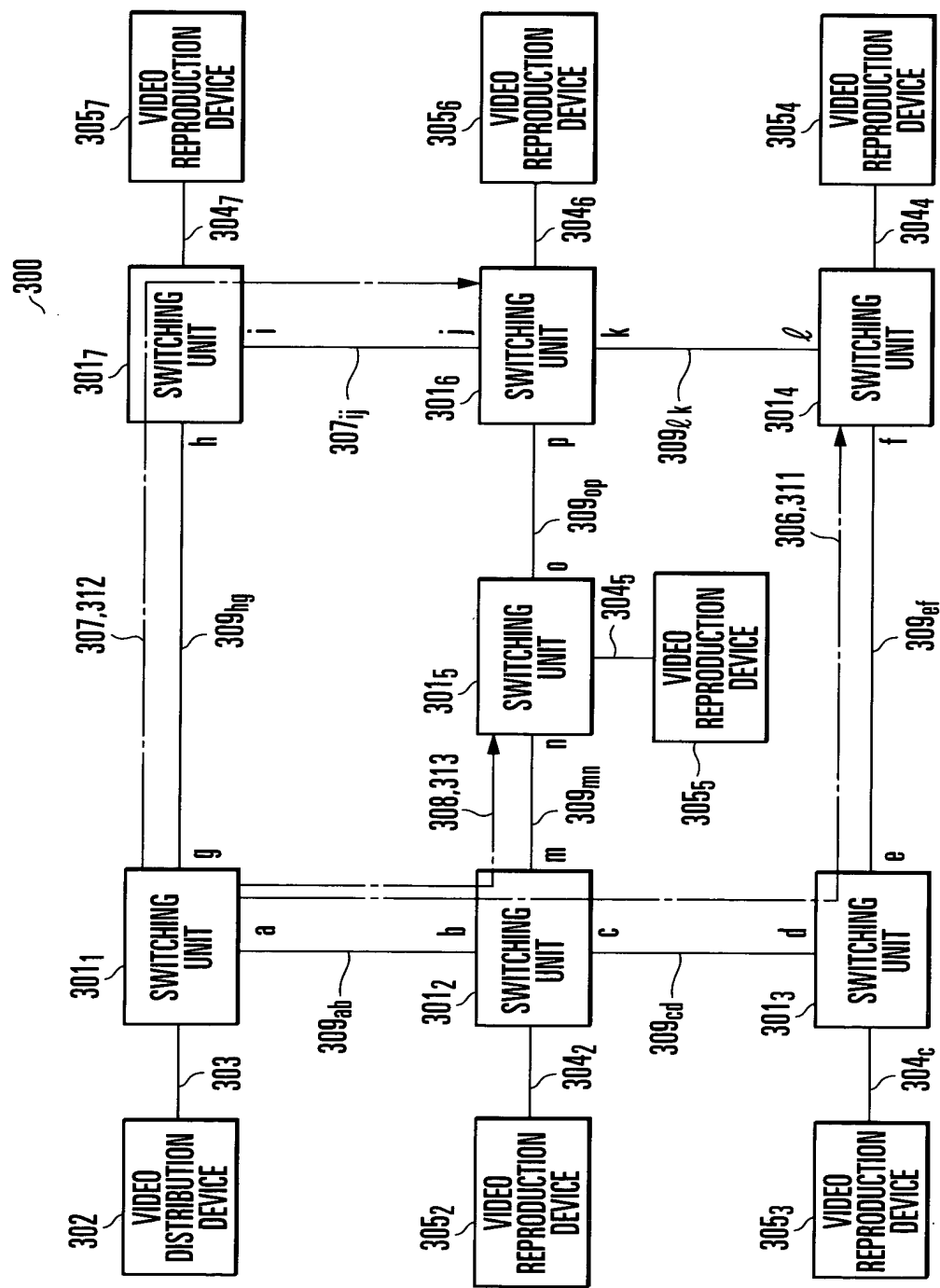
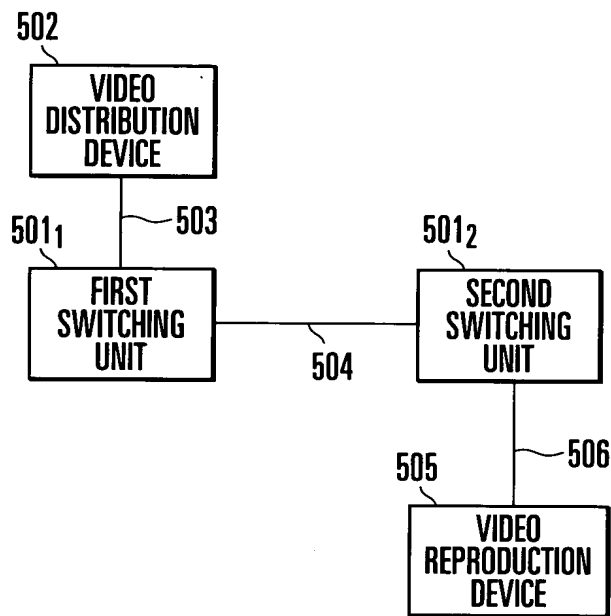


FIG. 9

PRIORITY	SECOND SWITCHING UNIT	THIRD SWITCHING UNIT	FOURTH SWITCHING UNIT	FIFTH SWITCHING UNIT	SIXTH SWITCHING UNIT	SEVENTH SWITCHING UNIT
"1"	b	d	f	n	j	h
"2"	c	e	ℓ	o	k	i
"3"	m				p	

(CLOCK SUPPLY LINE PRIORITY TABLE)

FIG. 10



**FIG. 11**  
**PRIOR ART**